

SST-PB3-PCIe-2

Hardware Reference Guide

Document Edition: 1.1

Document #: 715-0098

CI #: 1518

Document Edition: 1.1

Date: November 13, 2008

This document applies to the SST-PB3-PCle-2 interface card.

Copyright ©2008 Woodhead Industries Inc.

This document and its contents are the proprietary and confidential property of Woodhead Industries Inc. and/or its related companies and may not be used or disclosed to others without the express prior written consent of Woodhead Industries Inc. and/or its related companies.

SST is a trademark of Woodhead Industries Inc. All other trademarks belong to their respective companies.

At Woodhead, we strive to ensure accuracy in our documentation. However, due to rapidly evolving products, software or hardware changes occasionally may not be reflected in our documents. If you notice any inaccuracies, please contact us (see Appendix E).

**Written and designed at Woodhead Software & Electronics, 50 Northland Road,
Waterloo, Ontario, Canada N2V 1N3.**

Hardcopies are not controlled.

Contents

Preface	v
Purpose of this Guide	vi
Conventions	vi
Style.....	vi
Special Terms	vii
Special Notation	viii
 Card Overview	 9
1.1 Warnings and Cautions.....	10
1.2 Card Features.....	11
1.3 PCI Express Compatibility	12
1.4 Hardware Description.....	13
1.4.1 LEDs.....	14
1.4.2 Profibus Connector.....	15
 Installation	 17
2.1 System Requirements	18
2.2 Handling Precautions.....	18
2.3 Installing the Card	19
2.4 Connecting to a Profibus Network	20
2.4.1 DB9 Instructions	20
2.4.2 Termination	20
 Hardware Register Details	 21
3.1 PB3-PCIe-2 Card Configuration Registers	22

3.1.1 Host Register Layout.....	22
3.1.2 Control Register	23
3.1.3 AddrMatch Register	24
3.1.4 WinSize Register.....	24
3.1.5 Bank Address Register	25
3.1.6 HostIrq Register	25
3.1.7 LedReg Register	25
3.1.8 Debug Register	26
3.1.9 HDR Register	26
3.2 SST-PB3-PCIe-2 Configuration Space	26
Troubleshooting	29
4.1 SYS LED is Red.....	30
Error Messages	31
A.1 Introduction	32
A.2 HDR Messages	32
A.3 Fatal Hardware Self-Test Flash Codes	32
Technical Specifications	33
B.1 Technical Specifications	34
Loading Firmware	35
C.1 Firmware	36
C.1.1 Verify Card Presence.....	36
C.1.2 Load and Start the Firmware Module.....	37
CE Compliance	39
D.1 CE Compliance	40
Warranty and Support.....	41
E.1 Warranty.....	42
E.2 Reference Documents	42
E.3 Technical Support	42
E.3.1 Getting Help.....	43
Index.....	45

Preface

Preface Sections:

- Purpose of this Guide
- Conventions

Purpose of this Guide

This guide contains technical and product-related information on the SST-PB3-PCIE-2 interface card and derivatives.

The SST-PB3-PCIE-2 has its own CPU that executes downloadable firmware application modules. The main function of these modules is to enable application-level product behavior. For more details, refer to relevant firmware documentation.

Conventions

This guide uses stylistic conventions, special terms, and special notation to help enhance your understanding.

Style

The following stylistic conventions are used throughout this guide:

Bold	indicates field names, button names, tab names, executable files, and options or selections
<i>Italics</i>	indicates keywords (indexed) or instances of new terms and/or specialized words that need emphasis
CAPS	indicates a specific key selection, such as ENTER, TAB, CTRL, ALT, DELETE
Code Font	indicates command line entries or text that you'd type into a field
<u>Underlining</u>	indicates a hyperlink
">" delimiter	indicates how to navigate through a hierarchy of menu selections/options
"0x"	indicates a hexadecimal value

Special Terms

The following special terms are used throughout this guide:

<i>Card</i>	The SST-PB3-PCIe-2 network interface card
<i>Channel</i>	A Profibus network interface on the card
<i>DWORD</i>	Little Endian 32-bit value, unless otherwise stated.
<i>Firmware Module</i>	The embedded software module that gets loaded to the card's memory and runs on the card. This is the operating system of the card, enabling it to respond to commands from the host and manage network communications.
<i>Host</i>	The computer system in which the card is installed
<i>Shared Memory</i>	On-card memory mapped to the host computer and shared between the firmware module and the host application.
<i>.ss3</i>	An encoded firmware module for the card
<i>WORD</i>	Little Endian 16-bit value, unless otherwise stated.

Special Notation

The following special notations are used throughout this guide:



Warning

Warning messages alert the reader to situations where personal injury may result. Warnings are accompanied by the symbol shown, and precede the topic to which they refer.



Caution

Caution messages alert the reader to situations where equipment damage may result. Cautions are accompanied by the symbol shown, and precede the topic to which they refer.



Note

A note provides additional information, emphasizes a point, or gives a tip for easier operation. Notes are accompanied by the symbol shown, and follow the text to which they refer.

1

Card Overview

Chapter Sections:

- Warnings and Cautions
- Card Features
- Hardware Description

1.1 Warnings and Cautions

The card is an electrical component and must be treated with the following precautions:



Warning

Only qualified electrical personnel familiar with the construction/operation of this equipment and the hazards involved should install, adjust, operate, and/or service this equipment. Read and understand this guide in its entirety before proceeding. Failure to observe this precaution could result in severe bodily injury or, in extreme cases, loss of life.



Warning

You must provide an external, hand-wired emergency stop circuit outside the programmable controller circuitry. This circuit must disable the system in case of improper operation. Uncontrolled machine motion may result if this procedure is not followed. Failure to observe this precaution could result in bodily injury.



Caution

The card contains static-sensitive components. Careless handling may severely damage the card. Do not touch any of the connectors or pins on the card. When not in use, the card should be stored in an anti-static bag. Failure to observe this precaution could result in damage to or destruction of the equipment.

1.2 Card Features

The SST-PB3-PCIe-2 is the next-generation Profibus PCI card. It can perform the following functions:

- Act as a DP master
- Act as a DP slave (DPV0)
- Send and receive FDL (layer 2) messages
- Support Master DPV0 and DPV1 Class 1 and Master Class 2 messaging
- Support simultaneous operation in all of the above modes
- Support the standard Profibus baud rates of 9.6K, 19.2K, 31.25K, 45.45K, 93.75K, 187.5K, 500K, 1.5M, 3M, 6M and 12M baud

1.3 PCI Express Compatibility

Summary

The card's compatibility can be summarized as follows:

- Will operate in PCI Express 1.0-compliant systems
- Equipped with a single-lane PCI Express port
- Can be used in 1x (single-lane) or larger PCI Express slot

1.4 Hardware Description

The main features of the card are described in more detail in the following sections.

Figure 1: The SST-PB3-PCIe-2 Interface Card

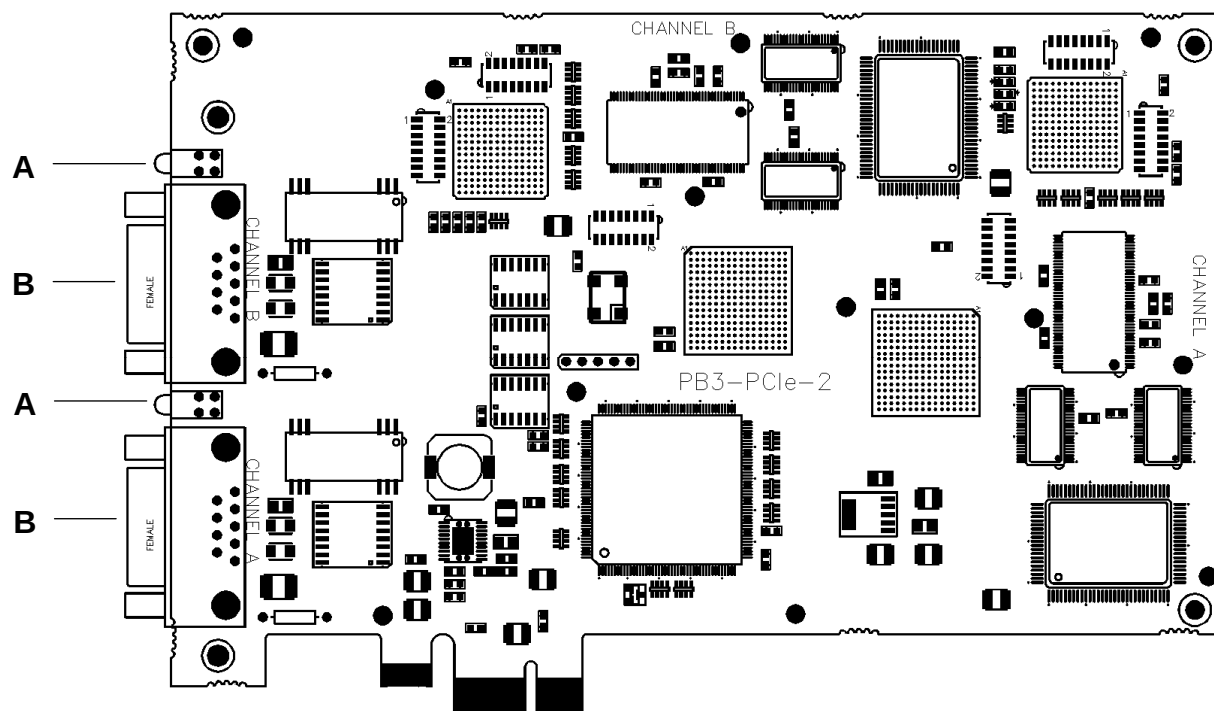


Table 1: Card Components

Feature	Description
A	LEDs
B	Profibus DB9 Connector



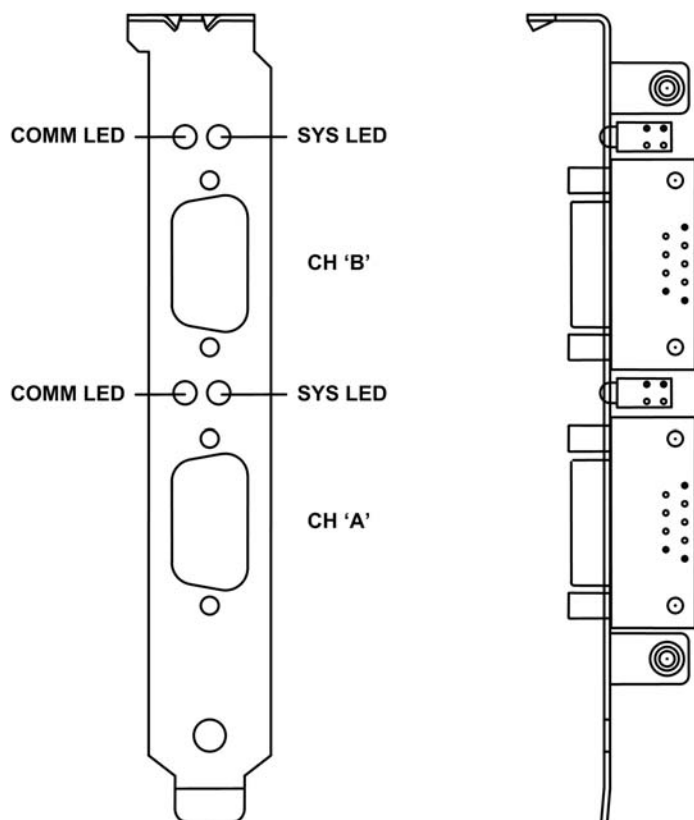
Note

The recommended male connector is the BradConnectivity MA9D00-42 Diagnostic D-Sub.

1.4.1 LEDs

The card has two LEDs: Communications (COMM), and System (SYS). The LEDs are identified in the following figure.

Figure 2: Card Bracket



During power-up, the COMM LED is off, and the SYS LED is amber. Once a firmware module has been loaded, the LEDs are under firmware control. Refer to the Firmware Reference Guide for more details.

If a fault is detected by the boot code during power-up, an error is flashed on the SYS LED. Refer to Section A.3, [Fatal Hardware Self-Test Flash Codes](#), for more details.



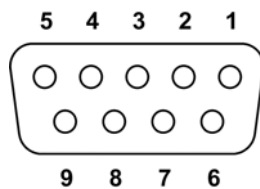
Note

For information on troubleshooting using LEDs, refer to Section 4.1, [SYS LED is Red](#).

1.4.2 Profibus Connector

The card contains a standard Profibus DB9 connector. Pin numbers are identified in the figure below.

Figure 3: The DB9 Connector



The recommended cable is Belden 3079A. Examples include:

- Brad Harrison 85-0001 PVR 2 conductor with shield, UL-listed Profibus cable
- Bosch Comnet DP #913 548 Flexible Profibus Cable
- Bosch Comnet DP #917 201 Trailing Profibus Cable
- Bosch Comnet DP #917 202 Massive Profibus Cable



Note

For instructions on connecting to a Profibus network, refer to Section 2.4, [Connecting to a Profibus Network](#).

2

Installation

Chapter Sections:

- System Requirements
- Handling Precautions
- Installing the Card
- Connecting to a Profibus Network

2.1 System Requirements

To install and operate the card, the following system requirements must be met:

- An available PCI Express 1x, 4x/8x or 16x lane slot. For details, refer to Section 1.3, [PCI Express Compatibility](#).
- One physical interrupt

2.2 Handling Precautions

The card contains components that are sensitive to electrostatic discharge (ESD). Do not touch the card without following these precautions:



Caution

- Always follow correct ESD procedures before handling the card. We strongly recommend the use of a grounding wrist strap.
- Never touch any of the card's connectors or pins. Handle the card by its edges.
- When the card isn't in your computer, always store it in its protective ESD bag.

2.3 Installing the Card

To install the SST-PB3-PCIe-2:

1. Ensure that all power to your computer is off.
2. Adequately ground yourself, as explained in Section 2.2, [Handling Precautions](#).
3. Unplug the power cord, modem (if applicable), and any network cables.
4. Remove the computer cover. Consult your computer user's guide for information on installing add-in boards.
5. Take the card out of its shipping container, being careful not to touch any of the connectors or pins.
6. Firmly press the card onto the PCI Express connector.
7. Secure the card to the case, according to the computer user's guide.
8. Replace the computer cover and plug any necessary cords and cables back in.

2.4 Connecting to a Profibus Network

2.4.1 DB9 Instructions

Table 2: DB9 Instructions

DB9 Pin Description	DB9 Pin #	DB9 Termination with SST-PB3-PCle-2
Chassis ground	1	-
Reserved	2	-
Data +	3	Connect this pin to Pin 8 (data -) with 220 ohm resistor
Tx enable	4	-
Isolated ground	5	Connect this pin to Pin 8 (data -) with 390 ohm resistor
Voltage plus	6	Connect this pin to Pin 3 (data +) with 390 ohm resistor
Reserved	7	-
Data -	8	-
Reserved	9	-

2.4.2 Termination

Always refer to Profibus documentation for proper network termination and wiring directions.

3

Hardware Register Details

Chapter Sections:

- PB3-PCIe-2 Card Configuration Registers

3.1 PB3-PCIe-2 Card Configuration Registers

This section provides hardware register details for the SST-PB3-PCIe-2 card.

3.1.1 Host Register Layout

The registers are stacked in memory for the two channels. Channel A is located in the first 8 bytes and Channel B is located in the next 8 bytes. The base memory in the host system can be found by reading the PCI configuration space at offset 0x18.

The channels' memory is also stacked. Channel A is located in the first 512 Kbytes and Channel B is located in the next 512 Kbytes. For PCI configuration space information, refer to Section 3.2, [SST-PB3-PCIe-2 Configuration Space](#).



Note

When power is applied to the card, or after a physical reset from the system, it may take up to 2 seconds for the card to initialize. Successful initialization can be confirmed by monitoring the LEDs or by reading the HDR register, as described in Section C.1.1, [Verify Card Presence](#).

Table 3: Host Register Layout

Offset	Register Name	Bit Name							
		7	6	5	4	3	2	1	0
00h	Control	CardRun	MemEn	IntEn	WdTout	HostIrq1	HostIrq0	CardIrq1	CardIrq0
01h	AddrMatch	1	AM18	AM17	AM16	AM15	AM14	Reserved	Reserved
02h	BankAddress	0	0	0	BA16	BA15	BA14	Reserved	Reserved
03h	WinSize	WS19	WS18	WS17	WS16	WS15	WS14	WS13	WS12
04h	HostIrq	Reserved	Reserved	Reserved	Reserved	IrqLevel			
05h	LedReg	Reserved	Reserved	Reserved	Reserved	CommRed	CommGrn	SYSRed	SYSGrn
06h	Debug	HWReset	Reserved	Reserved	JTAGEN	CPUTRST	CPUTMS	CPUTDI	CPUTCK
07h	HDR	HostDataReg (written by CPU)							
08h-01Fh	Reserved								

3.1.2 Control Register

This register is a group of control and status bits.

Table 4: Control Register Settings

Bit	7	6	5	4	3	2	1	0
Name	CardRun	MemEn	IntEn	WdTout	Reserved	HostIrq0	CardIrq1	CardIrq0
Read/Write	R/W	R/W	R/W	R	R/W	R/W	R/W	R/W
Reset	0	0	0	0	0	0	0	0

Table 5: Control Register Bit Descriptions

Bit Name	Description
CardRun	This bit controls and indicates whether or not the card's processor is running. It also affects the card's SYS LED. - When this bit is 0, the processor is halted, and the SYS LED is RED - When this bit is 1, the processor is running normally, and the LED is under processor control - When this bit is 1, and watchdog has timed out, processor is halted, and the SYS LED is RED This bit must remain low for at least 50 μs to guarantee proper reset.
MemEn	High (1) enables shared memory decoding of addresses in this board's range. This board's range is defined by the plug and play BIOS or operating system.
IntEn	High (1) enables interrupts on IrqLevel when a HostIrq bit is high (1). - Writing 1 enables interrupts - Writing 0 disables interrupts (the IrqPending flag still functions as described)
WdTout	WdTout high ('1') indicates that a watchdog timeout has occurred, or that the CPU has been held in RESET by some other means. To restore this bit to 0, clear CardRun.
Bit 3	Reserved.

Bit Name	Description
HostIrq0	This bit is used by the card processor to send interrupts to the host. • Writing 1 acknowledges the interrupt and clears it • Writing 0 has no effect • Reading 1 indicates interrupt in progress • Reading 0 indicates interrupt complete
CardIrq1	This bit is used by the host to send interrupts to the card processor. • Writing 1 generates an interrupt to the card • Writing 0 has no effect • Reading 1 indicates interrupt in progress • Reading 0 indicates interrupt complete
CardIrq0	This bit is used by the host to send interrupts to the card processor. • Writing 1 generates an interrupt to the card • Writing 0 has no effect • Reading 1 indicates interrupt in progress • Reading 0 indicates interrupt complete

3.1.3 AddrMatch Register

Table 6: AddrMatch Register Bit Descriptions

Bit Name	Description
AM19 – AM12	Reserved. This register always reads zero, and writing to these bits has no effect.

3.1.4 WinSize Register

Table 7: WinSize Register Bit Descriptions

Bit Name	Description
WS19-WS12	Reserved. This register always reads 0x3F, and writing to it has no effect.

3.1.5 Bank Address Register

This register is used to switch banks of shared memory into host memory space.

Table 8: Bank Address Register Bit Descriptions

Bit Name	Description
BA17-13	Reserved. This register always reads zero, and writing to these bits has no effect.

3.1.6 HostIrq Register

Table 9: HostIrq Register Bit Descriptions

Bit Name/ Value	Description
IrqLevel3-IrqLevel0	Reserved. This register always reads zero, and writing to it has no effect

3.1.7 LedReg Register

This register reflects the state of the card's LEDs, allowing the host software to monitor the LEDs and display them on-screen.

LedReg is controlled by firmware. Reading the register will reflect the following LED states.

Table 10: LedReg Register Settings

Bit	7	6	5	4	3	2	1	0
Name	Reserved				COMMRd	COMMGrn	SYSRed	SYSGrn
Read/Write	R	R	R	R	R	R	R	R
Reset	0	0	0	0	0	0	0	0

Table 11: SYS and COMM LED Status

Bit Name/Value		Description
SYSGrn	SYSRed	These bits indicate the state of the card's SYS LED.
0	0	LED is off
0	1	LED is red
1	0	LED is green
1	1	LED is amber
CommGrn	CommRed	These bits indicate the state of the card's COMM LED
0	0	LED is off
0	1	LED is red
1	0	LED is green
1	1	LED is amber

3.1.8 Debug Register

Reserved for future use.

3.1.9 HDR Register

Reserved for firmware use.

3.2 SST-PB3-PCIe-2 Configuration Space

Table 12: SST-PB3-PCIe-2 Configuration Space

PCI CFG Register Address	Register Function								PCI Writable
	31	24	23	16	15	8	7	0	
0x00	Device ID 0x2608				Vendor ID 0x1389				N
0x04	Status				Command				Y
0x08	Class Code				Revision ID				N

PCI CFG Register Address	Register Function							PCI Writable	
	31	24	23	16	15	8	7		0
0x0C	BIST	Header ID	PCI Latency	CacheLineSize					Y[7:0]
0x10	PCI Base Address 0 Memory, 128 bytes, Reserved							Y	
0x14	PCI Base Address 1 Not Used							Y	
0x18	PCI Base Address 2 Memory, 1M bytes Profibus Interface For details, refer to the relevant firmware documentation.							Y	
0x1C	PCI Base Address 3 Memory, Host Interface Registers For details, refer to Section 3.1, PB3-PCIe-2 Card Configuration Registers .							Y	
0x20	PCI Base Address 4 Not Used							Y	
0x24	PCI Base Address 5 Not Used							Y	
0x28	Cardbus CIS Pointer (Not Supported)							N	
0x2C	Subsystem Device ID 0x9030				Subsystem Vendor ID 0x10B5			N	
0x30	PCI Base Address for Local Expansion ROM							Y	
0x34	Reserved							N	
0x38	Reserved							N	
0x3C	Max_Lat	Min_Gnt		Interrupt Pin		Interrupt Line		Y[7:0]	



Note

For the function and typical uses of all other PCI configuration space registers, refer to the PCI specification and to your particular OS documentation.



Note

Typically, the PCI configuration space registers do not need to be written to by the host system driver. A plug and play BIOS and/or host operating system will ensure that there are no system resource conflicts.

4

Troubleshooting

Chapter Sections:

- SYS LED is Red

For a list of hardware-related errors that can be generated by the card, refer to Appendix A, [Error Messages](#).



Warning

Only qualified electrical personnel familiar with the construction/operation of this equipment and the hazards involved should install, adjust, operate, and/or service this equipment. Read and understand this guide in its entirety before proceeding. Failure to observe this precaution could result in severe bodily injury or, in extreme cases, loss of life.

4.1 SYS LED is Red

If the SYS LED is red, the card is not running or there's been a firmware run-time error. Check the WdTout bit for a timeout, and consult the appropriate firmware manual if necessary. If you continue to experience difficulties, contact [Technical Support](#).



Note

For information on LED flash codes, refer to Section A.3, [Fatal Hardware Self-Test Flash Codes](#).



Error Messages

Appendix Sections:

- Introduction
- HDR Messages
- Fatal Hardware Self-Test Flash Codes

A.1 Introduction

The following errors may be reported during the card's startup self-test. Error messages are posted in the host interface's message area (0x0200) and can be displayed using the status applications provided with the card.

A.2 HDR Messages

If HDR reads anything other than 0x41, the card hasn't been found. If you continue to experience difficulties, contact [Technical Support](#).

A.3 Fatal Hardware Self-Test Flash Codes

Fatal failures during startup are accompanied by one of the following 8-bit fault codes, flashed on the SYS LED. Bit 0 is flashed first.

Table 13: LED Flash Codes

Value	Name	Description
01	BITTEST8	Bit test failure of an 8-bit memory range
02	BITTEST16	Bit test failure of a 16-bit memory range
03	BITTEST32	Bit test failure of a 32-bit memory range
04	ADDRTEST8	Address test failure of an 8-bit memory range
05	ADDRTEST16	Address test failure of a 16-bit memory range
06	ADDRTEST32	Address test failure of a 32-bit memory range
07-10	-	Reserved for future fatal start-up errors
11	JTAG_UNKNOWN	JTAG programming error
12	JTAG_TDOMISMATCH	JTAG output data failed to match expected pattern
13	JTAG_MAXRETRIES	JTAG output data failed to match expected pattern after several attempts
14	JTAG_ILLEGALCMD	JTAG programming file contained an unknown/malformed command
15	JTAG_ILLEGALSTATE	JTAG programming file commanded an illegal TAP state transition
16	JTAG_DATAOVERFLOW	JTAG programming file contained a shift pattern in excess of MAX_LEN * 8 bits
20-FF	-	Reserved for firmware-specific fatal errors

B

Technical Specifications

Appendix Sections:

- Technical Specifications

B.1 Technical Specifications

Table 14: Environmental Specifications

Ambient Conditions	Storage temp:	-40°C to +85°C
	Operating temp:	0°C to 55°C
	Humidity:	5% to 95% non-condensing
Typical Current Draw		400mA @ 12V 1.2A @ 3.3V
PCI Express Compliance		Compliant with PCI Express 1.0 For more information, refer to Section 1.3, PCI Express Compatibility .

Table 15: Network Specifications

Isolation	1000V
Protocol	RS485
Data Rate	All Profibus data rates up to 12Mbps

Table 16: PCI Bus Specifications

Dimensions	Height:	4.2 inches
	Width:	6.875 inches
Resources	PCI Region 0 = 128 bytes of PCI memory PCI Region 2 = 512k of 16-bit PCI memory PCI Region 3 = 32 bytes of 8-bit PCI memory One PCI interrupt	

C

Loading Firmware

Appendix Sections:

- Loading Firmware



Note

This appendix describes how to load the card manually, or how to write your own loader. If you are using a Windows loader provided by Molex, the following instructions are not required.

C.1 Firmware

Firmware modules for the SST-PB3-PCIe-2 card are supplied as .ss3 files, found on the software CD-ROM or on the website at <http://www.woodhead.com>.

If you are developing a driver for the card or producing a stand-alone embedded application, the following section describes the basic sequence of steps to manually load a module onto the card.



Note

For register descriptions, refer to Chapter 3, [Hardware Register Details](#).

C.1.1 Verify Card Presence

To verify the card's presence, follow these steps:

1. Start up your computer.
2. After the backplane reset has released, wait 2 seconds.
3. Query the BIOS/OS for a PCI card with Vendor ID 0x1389 and Device ID 0x2608.
For more information, refer to Section 3.2, [SST-PB3-PCIe-2 Configuration Space](#).

C.1.2 Load and Start the Firmware Module

To load and start the firmware module, follow these steps for each channel:

1. Bit-wise AND value 0x40 to the Control Register.
2. Write the contents of the entire firmware file into shared memory, starting at offset zero (0). The location of shared RAM can be found by reading PCI Base Address 2 from the SST-PB3-PCIe-2 configuration space. Channel A begins at this address and is located in the first 8 bytes. Channel B is offset 512KB from this address, and is located in the next 8 bytes. For details, refer to Section 3.2, [SST-PB3-PCIe-2 Configuration Space](#).
3. If the application requires interrupts from the card, write the interrupt level to the HostIrq register and bit-wise OR value 0x20 (IntEn) to the Control register.
4. Bit-wise OR value 0xC0 (CardRun and MemEn) to the Control register to start the firmware module.
5. Start a 2-second timeout timer and wait for the Command Register, at offset 0x8000, to read 0xE0 (command offline).
6. If the timer expires, the firmware module failed to start. Write zero (0) to the Control register to disable the card. If this problem persists, contact [Technical Support](#) for assistance.
7. Check the load status, as per the firmware manual.

D

CE Compliance

Appendix Sections:

- CE Compliance

D.1 CE Compliance

This device meets or exceeds the requirements of the following standard:

- EN 61326-1:2006 - “Electrical equipment for measurement, control and laboratory use - EMC requirements”.



Warning

This is a Class A product. In a domestic environment this product may cause radio interference in which case you may be required to take adequate measures.



Caution

This equipment is neither designed for, nor intended for operation in installations where it is subject to hazardous voltages and hazardous currents.

Marking of this equipment with the symbol **CE** indicates compliance with European Council Directive 89/336/EEC - The EMC Directive as amended by 92/31/EEC and 93/68/EEC.



Note

To maintain compliance with the limits and requirements of the EMC Directive, it is required to use quality interfacing cables and connectors when connecting to this device. Refer to the cable specifications in the Hardware Guide for selection of cable types.



Warranty and Support

Appendix Sections:

- Warranty
- Reference Documents
- Technical Support

E.1 Warranty

For warranty information, refer to <http://www.mysst.com/warranty.asp>.

E.2 Reference Documents

For information on Profibus, refer to one of the following:

- Profibus standard DIN 19 245 parts 1, 2 and 3. Part 1 describes the low-level protocol and electrical characteristics, Part 2 describes FMS, and Part 3 describes DP.
- European Standard EN 50170
- Profibus-DP Extensions to EN 50170 (DPV1)

E.3 Technical Support

Please ensure that you have the following information readily available before calling Technical Support:

- Card type and serial number
- Computer's make, model, CPU speed and hardware configuration (other cards installed)
- Operating system type and version
- Details of the problem you are experiencing: firmware module type and version, target network, and circumstances that may have caused the problem

E.3.1 Getting Help

Technical Support is available during regular business hours by telephone, fax or email from any Woodhead Software & Electronics office, or from <http://www.woodhead.com>. Documentation and software updates are also available on the website.



Note

If you are using the card with a third-party application, refer to the documentation for that package for information on configuring the software for the card.

North America

Canada:

Tel: +1-519-725-5136

Fax: +1-519-725-1515

Email: WoodheadSupportNA@molex.com

Europe

France:

Tel: +33 2 32 96 04 22

Fax: +33 2 32 96 04 21

Email: WoodheadIC.SupportEU@molex.com

Germany:

Tel: +49 7252 9496 555

Fax: +49 7252 9496 99

Email: WoodheadIC.SupportEU@molex.com

Italy:

Tel: +39 010 5954 052

Fax: +39 02 664 00334

Email: WoodheadIC.SupportIT@molex.com

Other countries:

Tel: +33 2 32 96 04 23

Fax: +33 2 32 96 04 21

Email: WoodheadIC.SupportEU@molex.com

Asia-Pacific

Japan:

Tel: +81 52 221 5950

Fax: +81 46 265 2429

Email: WoodheadIC.SupportAP@molex.com

Singapore:

Tel: +65 6268 6868

Fax: +65 6261 3588

Email: WoodheadIC.SupportAP@molex.com

China:

Tel: +86 21 5835 9885

Fax: +86 21 5835 9980

Email: WoodheadIC.SupportAP@molex.com

For the most current contact details, please visit <http://www.woodhead.com>.

Index

A

AddrMatch register, 24

B

Bank Address register, 25
bracket, 14

C

cable, recommended, 15
card
 capabilities of, 11
 cautions for, 10, 18, 40
 CE compliance and, 40
 connecting to network, 20
 defined, vii
 error messages on, 32
 handling, 10
 hardware features of, 13
 installing, 19
 loading firmware on, 36
 PCI compatibility and, 34
 PCI compatibility and, 12
 reference documents for, 42
 registers, 22
 system requirements for, 18

 technical specifications of, 34
 technical support for, 42
 troubleshooting, 30
 warnings for, 10, 18, 29, 40
 warranty for, 42
cautions
 defined, viii
 electrostatic discharge, 10, 18
 hazardous currents, 40
CE compliance, 40
channel, defined, vii
COMM LED
 described, 14
 status of, 26
compatibility, PCI, 12
components, of card, 13
configuration space, 26
connecting to Profibus network, 20
connector, for network, 15, 20
Control register, 23
current draw, 34

D

DB9 connector, 15, 20
Debug register, 26
dimensions, of card, 34
DWORD, defined, vii

E

electrostatic discharge (ESD), 10, 18
emergency stop circuit, 10
environmental specifications, of card, 34
error messages, 32

F

features, of card, 11
firmware module
 defined, vii
 loading on card, 36
 starting on card, 37
flash codes, fatal, 32

G

grounding wrist strap, 18

H

handling the card, 10, 18
hardware, described, 13
hazardous currents, 40
HDR register, 26
HDR, errors on, 32
height, of card, 34
Host register, 22
host, defined, vii
HostIrq register, 25
humidity, 34

I

installing
 card, 19
 firmware, 36
interrupts, 18, 23, 25, 34
isolation, network, 34

L

LedReg register, 25
LEDs
 description of, 14
 flash codes on, 32
 status of, 26

troubleshooting with, 30

M

memory, 24, 34
module, defined, vii

N

network
 connecting to, 20
 connector for, 15
 termination for, 20
 wiring, 20
network specifications, 34

O

operating temperature, 34

P

PB3-PCIe-2. *See* card
PCI bus specifications, of card, 34
PCI compatibility, of card, 12
physical interrupt, 18
precautions, 10, 18, 40
Profibus network, connecting to, 20
protocol, network, 34
purpose of this guide, vi

R

radio interference, 40
reference documents, 42
registers
 AddrMatch, 24
 Bank Address, 25
 configuration space and, 26
 Control, 23
 Debug, 26
 HDR, 26
 Host, 22
 HostIrq, 25
 LedReg, 25
 reserved, 26
 WinSize, 24
requirements, system, 18

S

- shared memory, defined, vii
- special notation, viii
- special terms, vii
- ss3 files, defined, vii
- SST-PB3-PCle-2. *See* card
- storage temperature, 34
- support, for card, 42
- SYS LED
 - described, 14
 - status of, 26
 - troubleshooting with, 30
- system requirements, for card, 18

T

- technical specifications, 34
- technical support, 42
- temperature
 - operating, 34
 - storage, 34
- termination, for Profibus network, 20
- testing card presence, 36

- troubleshooting, with SYS LED, 30

U

- uploading firmware, 36

V

- verifying card presence, 36

W

- warnings
 - card handling, 10, 29
 - defined, viii
 - emergency stop circuit, 10
 - radio interference, 40
- warranty, 42
- wattage, 34
- width, of card, 34
- WinSize register, 24
- wiring, for network, 20
- WORD, defined, vii
- wrist strap, 18